

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant : Hisashi Ohtani

Art Unit : 2815

Serial No. : 09/389,393

Examiner : Bradley Baumeister

Filed : September 3, 1999

Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE  
ELECTRODE

Commissioner for Patents  
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# APPENDIX A (FORM PTO-1449)

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07977-204002

Substitute Form PTO-1449  
(Modified)U.S. Department of Commerce  
Patent and Trademark OfficeAttorney's Docket No.  
07977-204002Application No.  
09/389,393**Information Disclosure Statement  
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant  
Hisashi OhtaniFiling Date  
September 3, 1999Group Art Unit  
2815**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,177,577	01/05/93	Taniguchi et al.			
	AB	5,272,100	12/21/93	Satoh et al.			
	AC	5,308,998	05/03/94	Yamazaki et al.			
	AD	5,422,505	06/06/95	Shirai			
	AE	5,430,320	07/04/95	Lee			
	AF	5,481,121	01/02/96	Zhang et al.			
	AG	5,518,805	05/21/96	Ho et al.			
	AH	5,518,936	05/21/96	Yamamoto et al.			
	AI	5,580,800	12/03/96	Zhang et al.			
	AJ	5,612,234	03/18/97	Ho			
	AK	5,619,045	04/08/97	Konuma et al.			
	AL	5,624,137	02/18/97	YAMAZAKI et al.			
	AM	5,643,817	07/01/97	Kim et al.			
	AN	5,668,032	09/16/97	Holmberg et al.			
	AO	5,670,795	09/23/97	Ikedo			
	AP	5,731,216	03/24/98	Holmberg et al.			
	AQ	5,741,736	04/21/98	Orlowski et al.			
	AR	5,742,363	04/21/98	Bae			

**Foreign Patent Documents or Published Foreign Patent Applications**

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AS	JP 406275641A	08/30/94	JAPAN				

**Other Documents (include Author, Title, Date, and Place of Publication)**

Examiner Initial	Desig. ID	Document
	AT	
	AU	

Examiner Signature

Date Considered

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.